

[Abstract] [PDF Full-Text (136 KB)] JNL

2 Single-chip 622-Mb/s SDH/SONET framer, digital cross-connect and add/drop multiplexer solution

Baechtold, P.H.; Beakes, M.P.; Buchmann, P.; Clauberg, R.; Ewen, J.F.; Gilsdorf, J.F.; Hauviller, P.; Herkersdorf, A.; Le Garrec, J.-C.; Lemppenau, W.; Parker, B.; Pearson, D.J.; Pereira, J.M.; Plassat, D.; reynolds, S.K.; Schindler, H.R.; Steimle, A.; We
Solid-State Circuits, IEEE Journal of, Volume: 36 Issue: 1, Jan. 2001

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[Abstract] [PDF Full-Text (140 KB)] JNL

3 Reducing misframe frequency for HEC-based variable length frame suitable for IP services

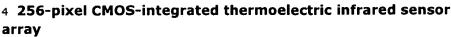
Ueda, H.; Yamaguchi, U.; Watanabe, R.

Communications, 2001. ICC 2001. IEEE International Conference on ,

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5 A scalable SDH/SONET framer architecture for datacom and telco applications

Herkersdorf, A.; Buchmann, P.; Clauberg, R.; Lemppenau, W.; Schindler, H.R.; Webb, D.
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6 A 3 GHz, 32 dB CMOS limiting amplifier for SONET OC-48 receivers

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7 A 0.6 W 10 Gb/s SONET/SDH bit-error-monitoring LSI

Kawai, K.; Ichino, H.

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ISSCC. 2000 IEEE International, 2000

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[Abstract] [PDF Full-Text (223 KB)] CNF

6 0.6 /spl mu/m CMOS, 622/155 Mbit/s ATM-SDH/SONET framer IC

Koziotis, M.; Birbas, A.; Theoharis, S.

Electronics Letters, Volume: 35 Issue: 21, 14 Oct. 1999

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Chan Kim; Jong Arm Jun; Sang Ho Lee; Jae Geun Kim ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on , 1999

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[Abstract] [PDF Full-Text (320 KB)] CNF

10 The chip set HDSL modem: design and performance evaluation

Krstic, V.R.; Kamsek, U.A.; Kuzmanovic, A.Z. Telecommunications in Modern Satellite, Cable and Broadcasting Services, 1999. 4th International Conference on , Volume: 1 , 1999 Page(s): 98 -101 vol.1

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11 A 10 Gb/s SiGe bipolar framer/demultiplexer for SDH systems

Shioiri, S.; Soda, M.; Morikawa, T.; Hashimoto, T.; Sato, F.; Emura, K.

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12 Design and implementation of a high-speed ATM host interface controller

Chan Kim; Jong-Arm Jun; Yeong-Ho Park; Kyu-Ho Lee; Hyup-Jong Kim

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[Abstract] [PDF Full-Text (364 KB)] CNF

15 10 Gb/s framer/demultiplexer IC for SONET STS-192 applications

Bagheri, M.; Kong, D.T.; Hacker, J.; Schneider, K.; Chow, J. Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995, 1995

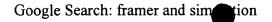
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[Abstract] [PDF Full-Text (520 KB)] CNF

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... has been very difficult to find with simulation since it was only found after several

months of simulation at the top level (simulation of the whole framer). ...

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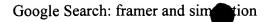
... 408) 544-7104 Literature Services: lit_req@altera.com T3 Framer MegaCore Function ... and-route constraints (where necessary) I Secure RTL simulation model I ...

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www.xilinx.com/ipcenter/catalog/ logicore/docs/t1e1_framer.pdf - Similar pages

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... CCSDS Framer Model*. CCSDS Framer Simulation Model. Attached Sync Marker. Header.
Data. ... HDLC Framer Model. HDLC Framer Simulation Model. IP Packet. Flag Field. Header. ...
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